
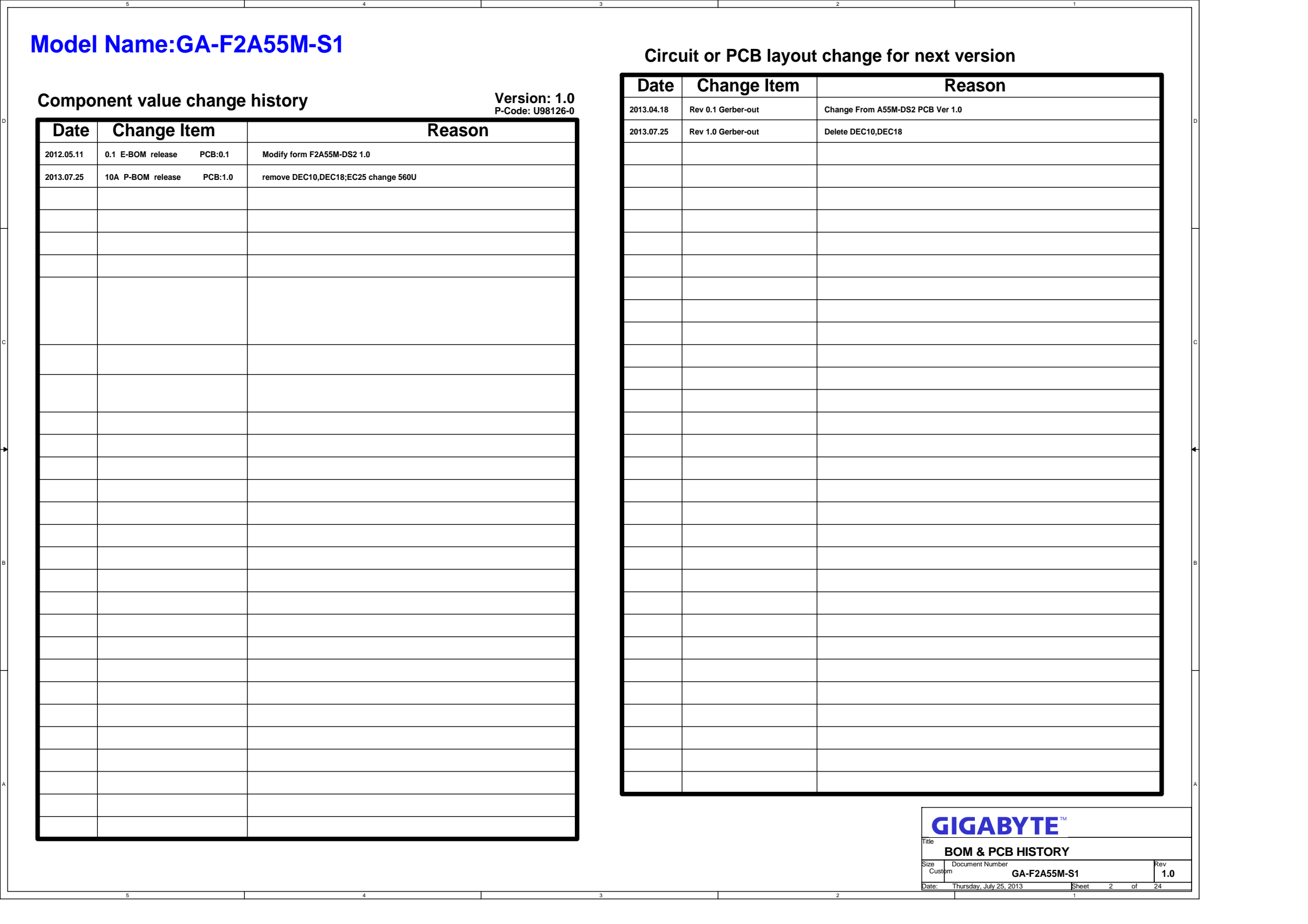


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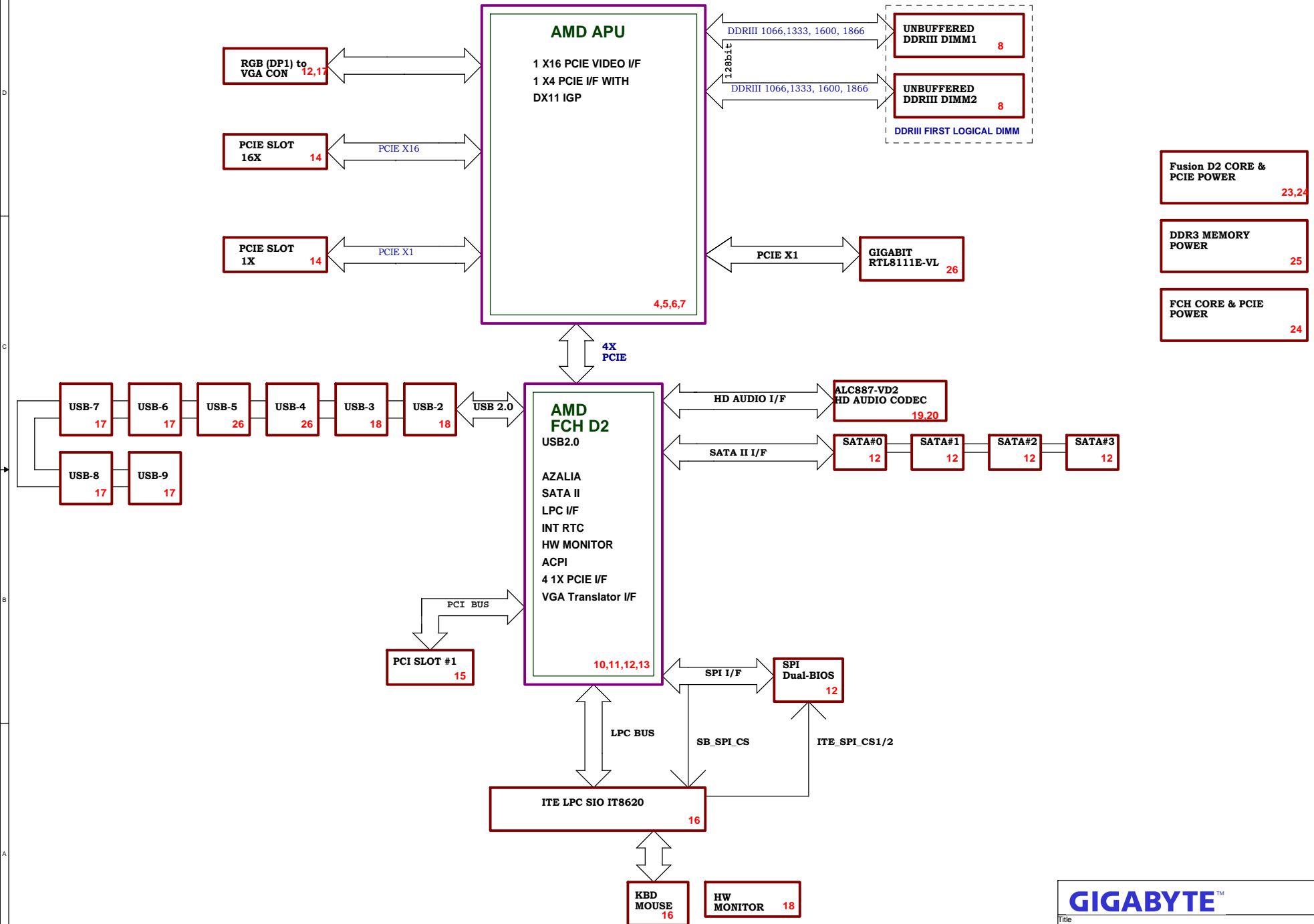
PAGE	TITLE	Revision :1.0
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03	BLOCK DIAGRAM	
04	APU DDRIII MEMORY	
05	APU CONTROL	
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19	ATX, FRONT PANEL	
20	POWER SEQUENCE	
21	VCORE (ISL62773)	
22	VCORE MOS	
23	VCC_SB , APU_VDDPA , VCC11_DUAL , VDDA25	
24	DDR POWER, VCC18 , EUP	
25	RTL8111F-VL	

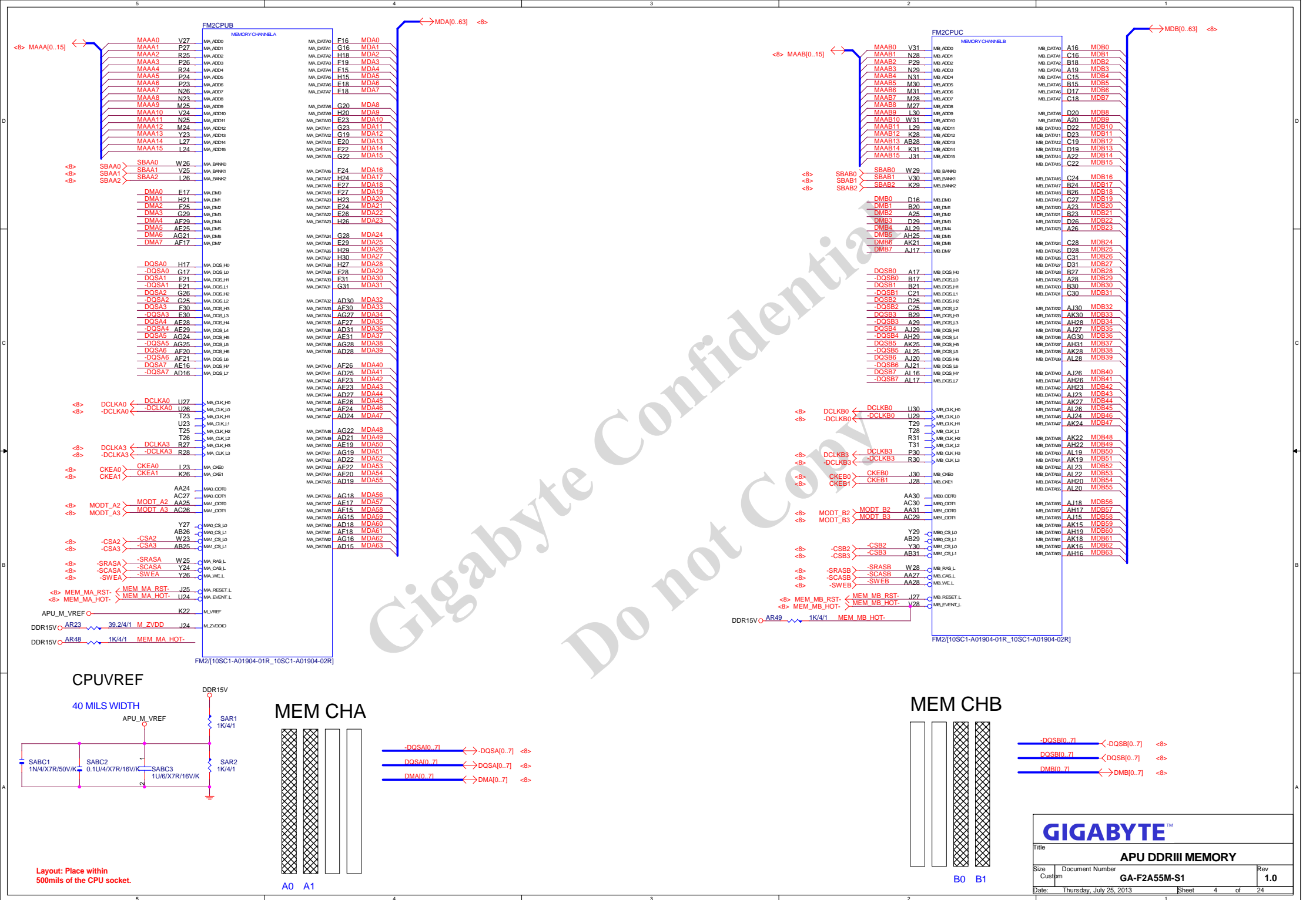
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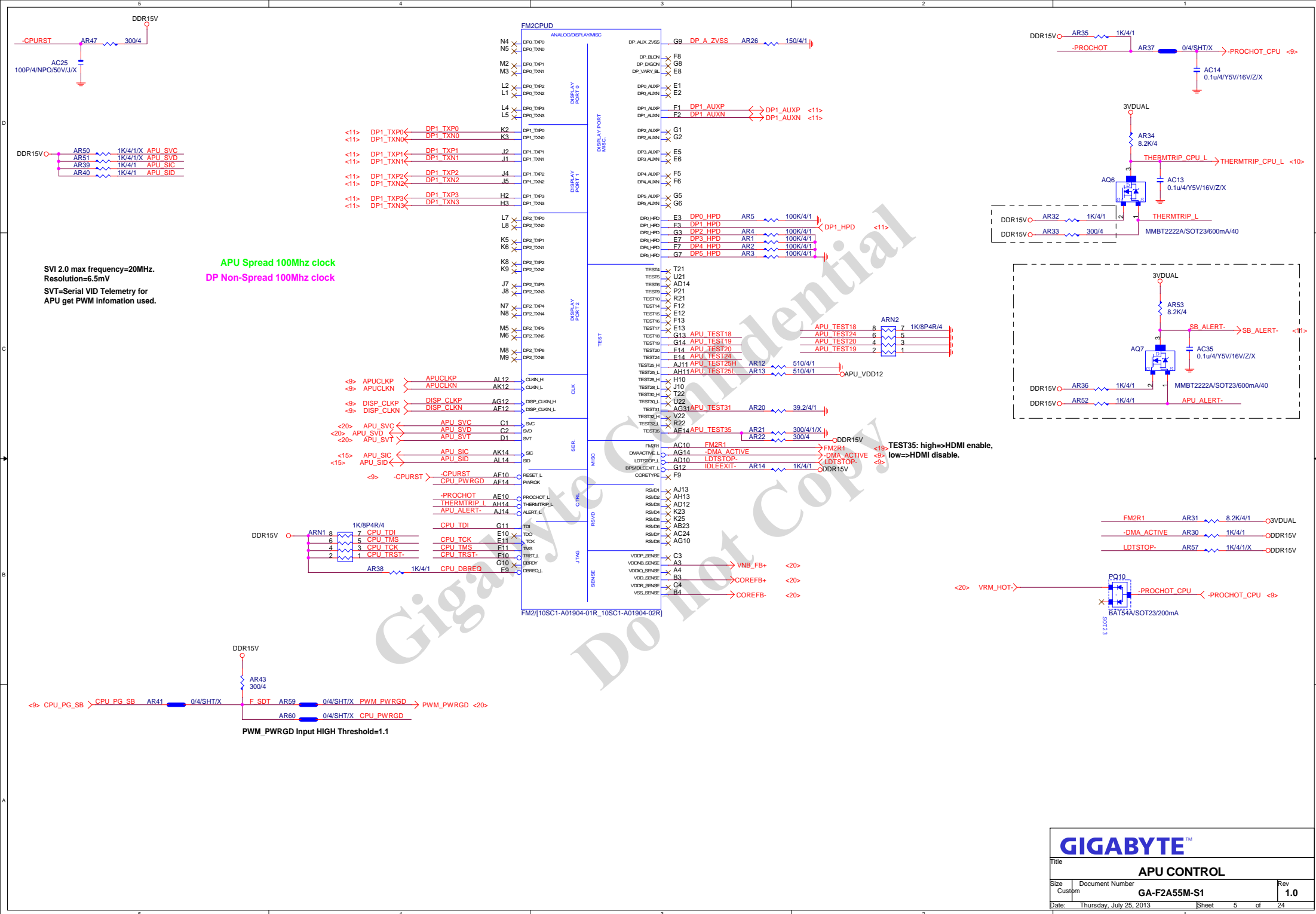
			
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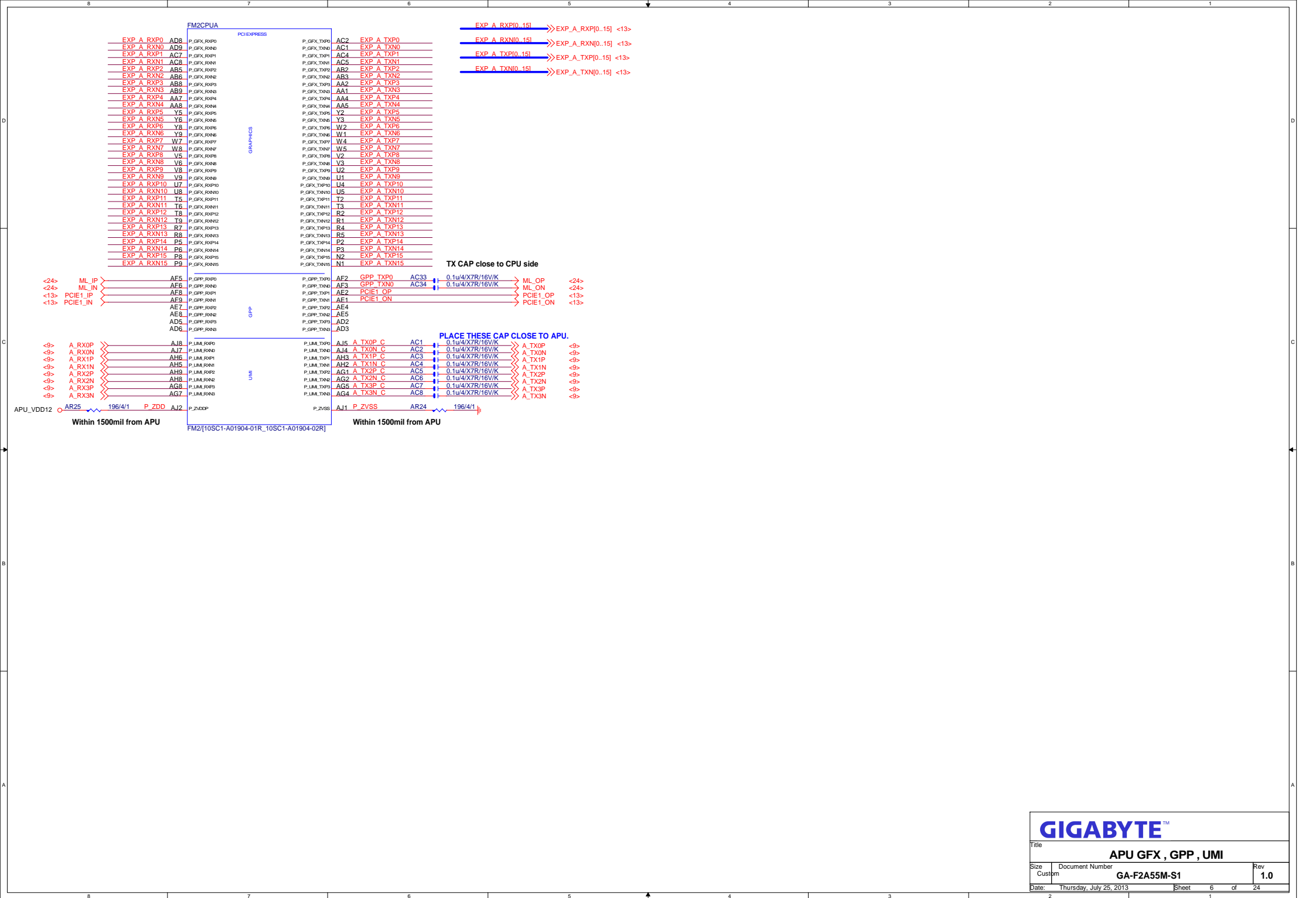
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AMD Fusion Hudson CUSTOMER DESKTOP REFERENCE DESIGN

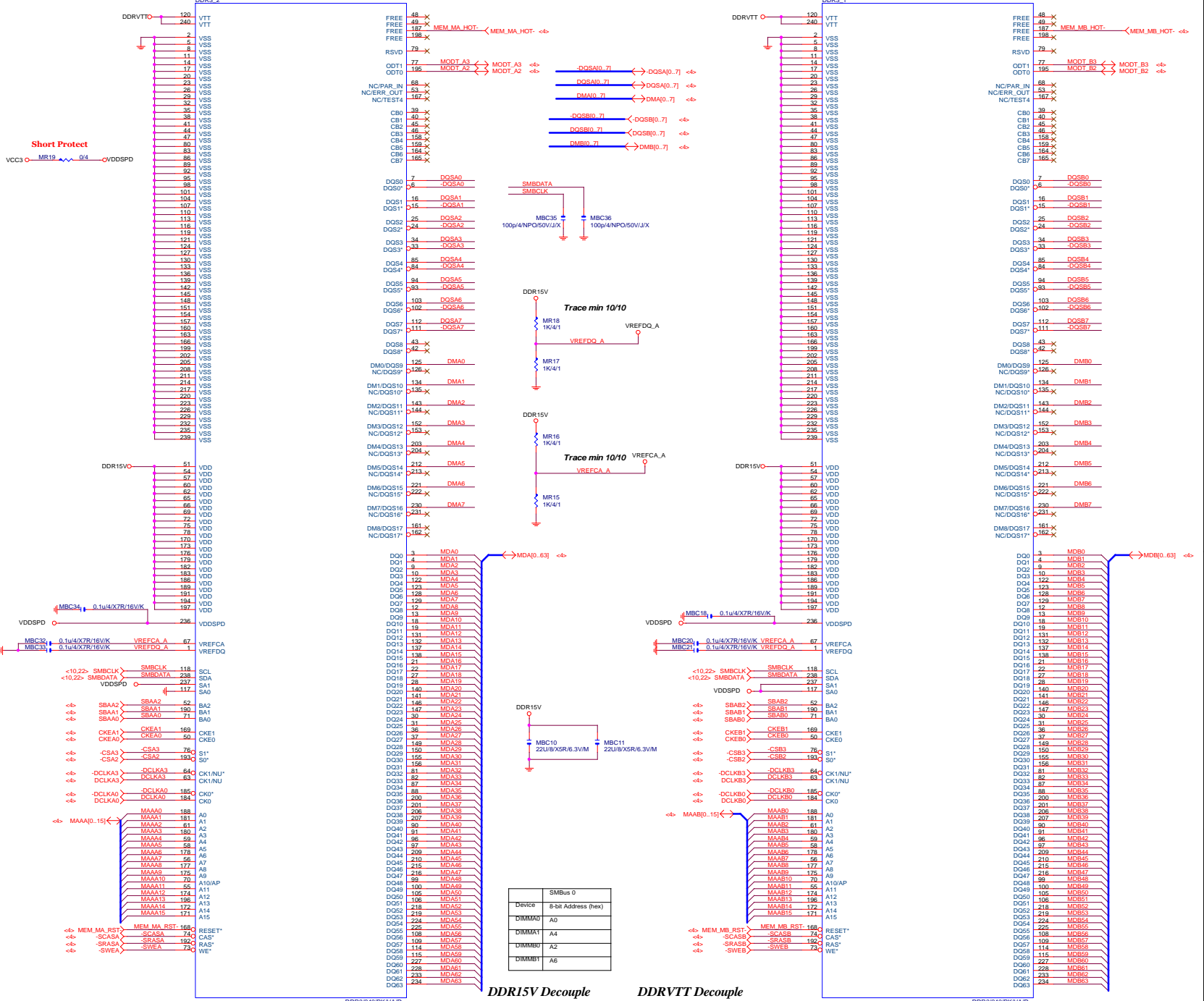






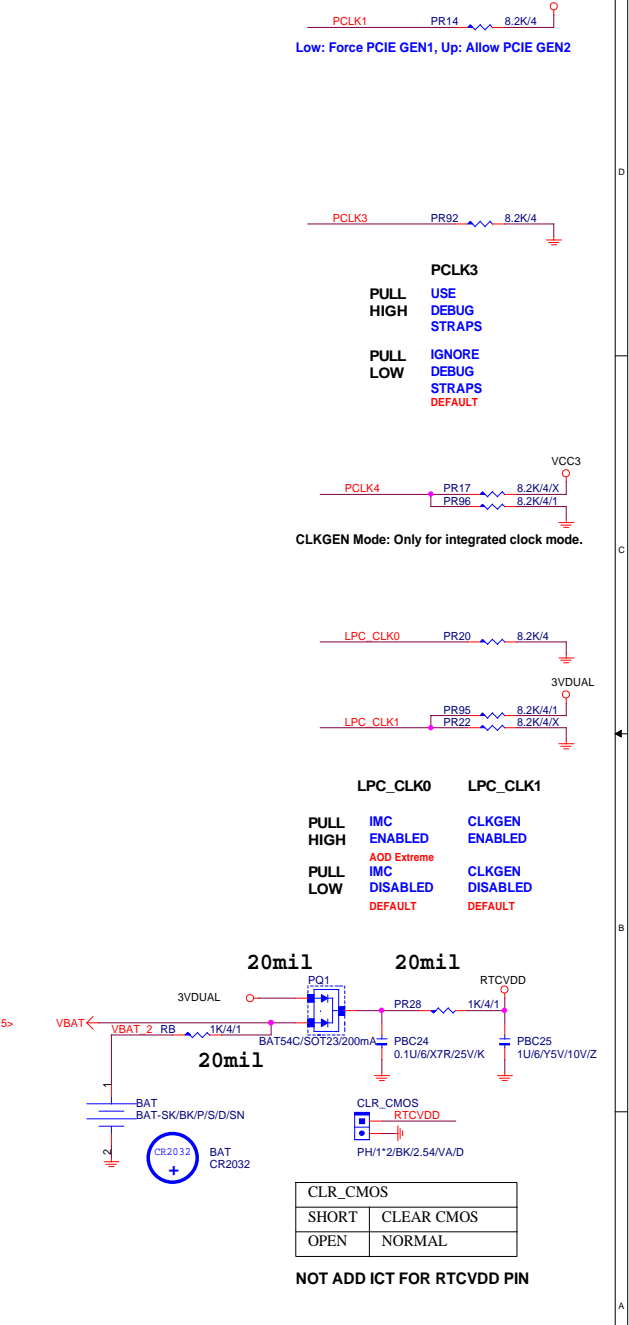
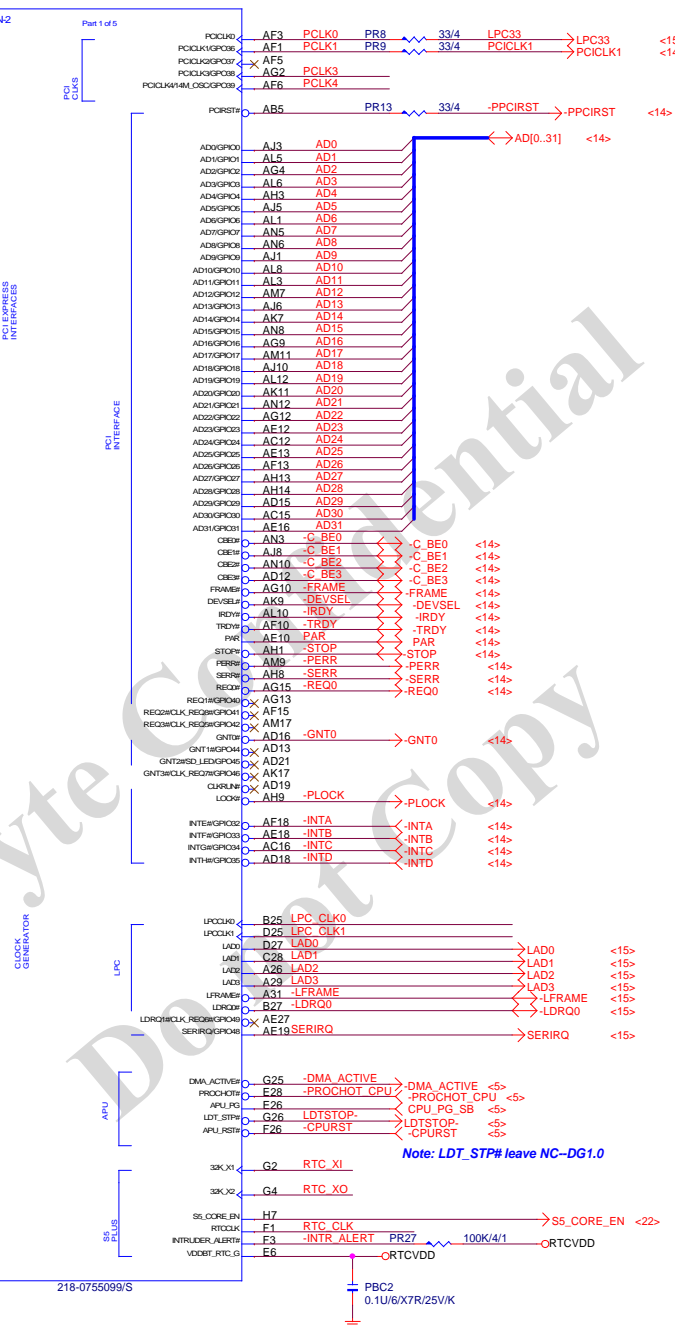
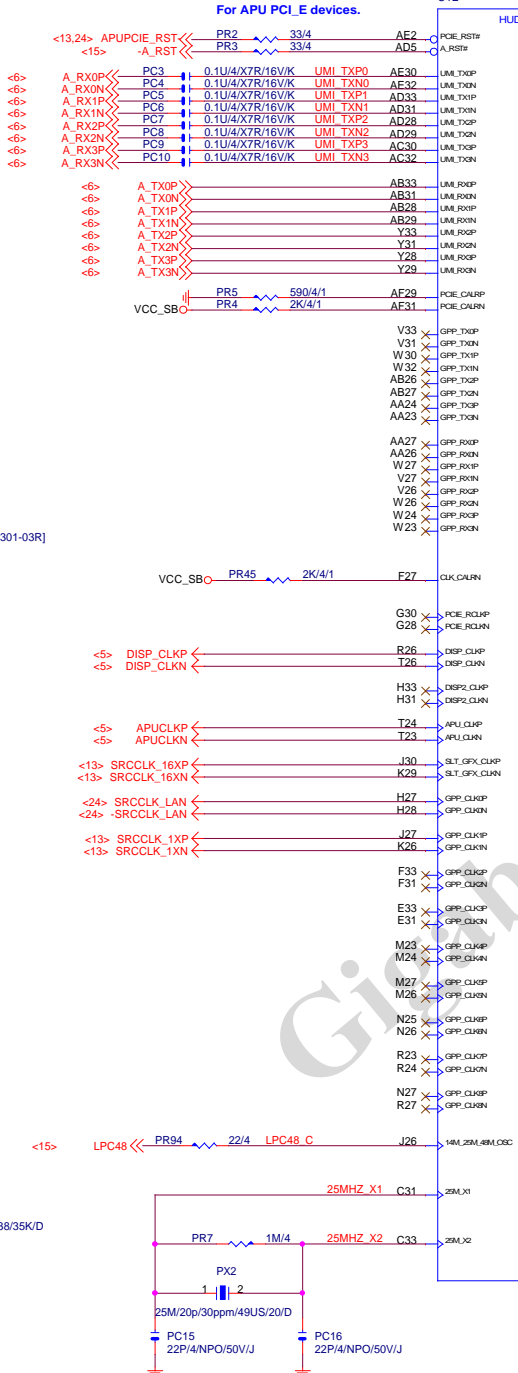
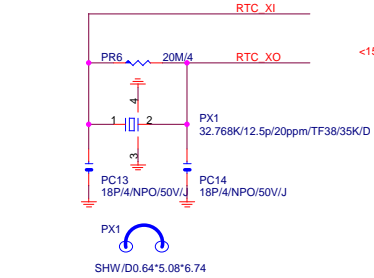
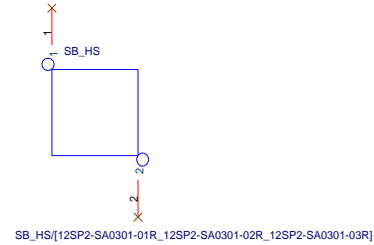






PLACE THESE PCIE AC COUPLING CAPS CLOSE TO SB850

S.B HEATSINK

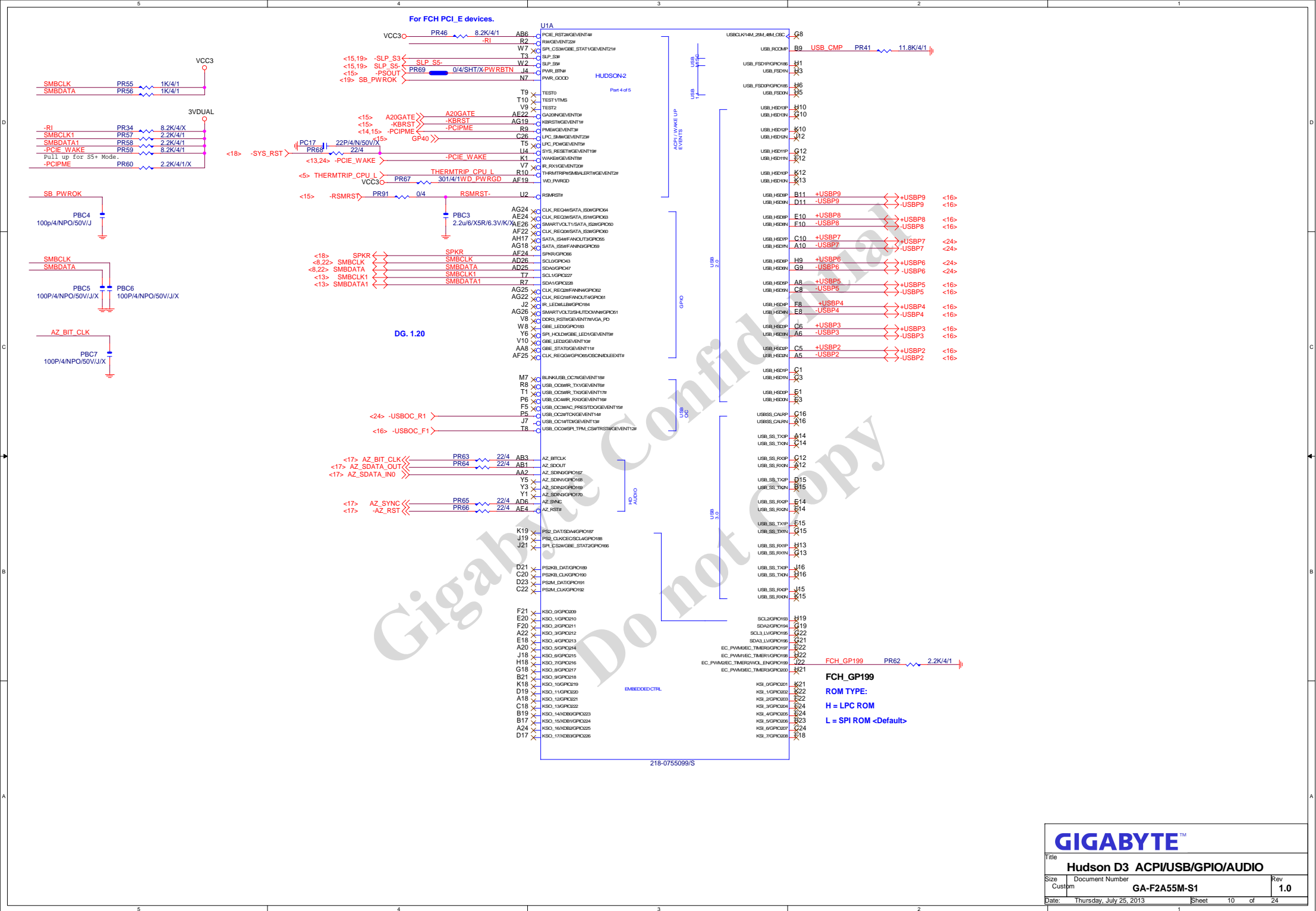


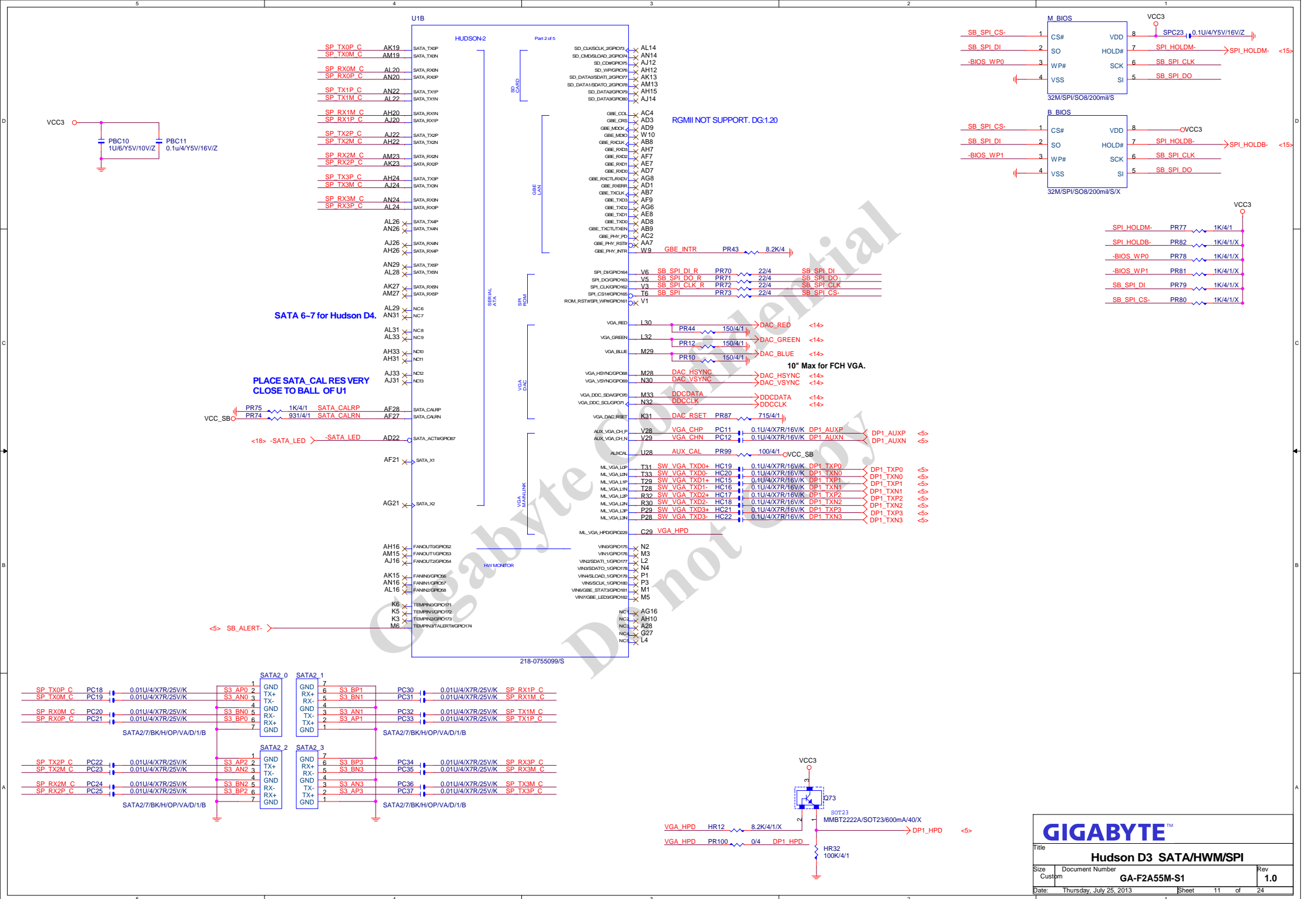
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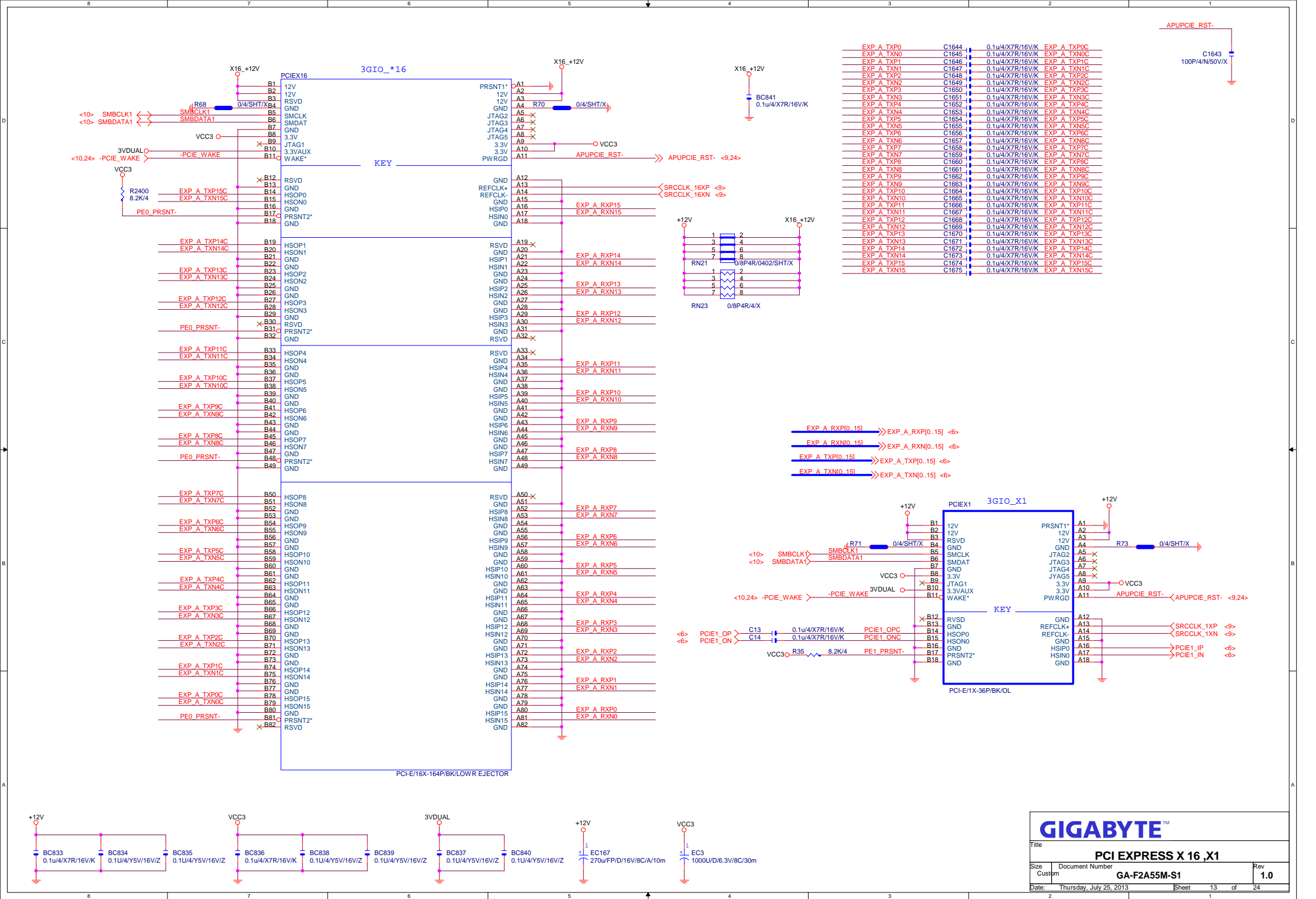
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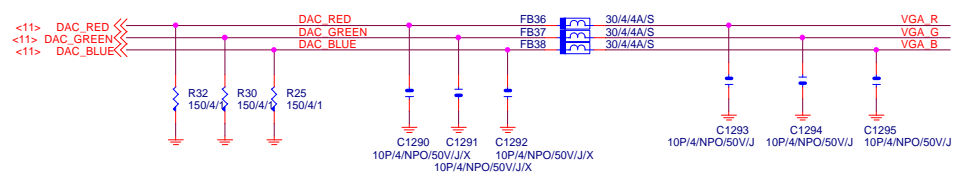
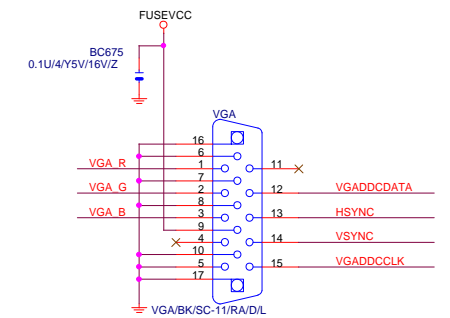
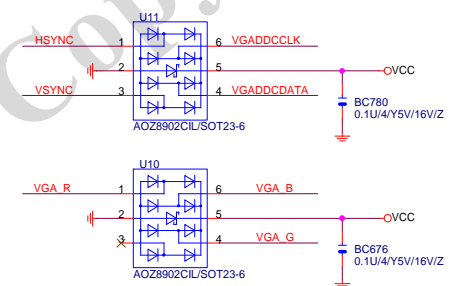
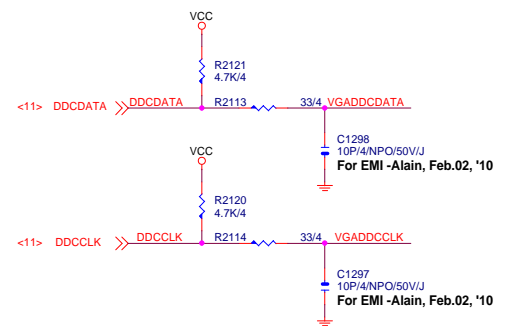
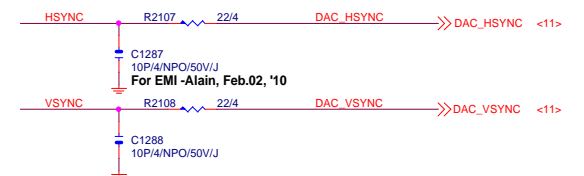
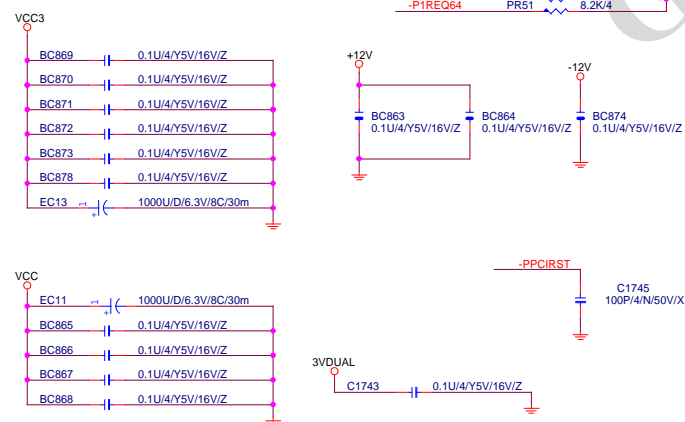
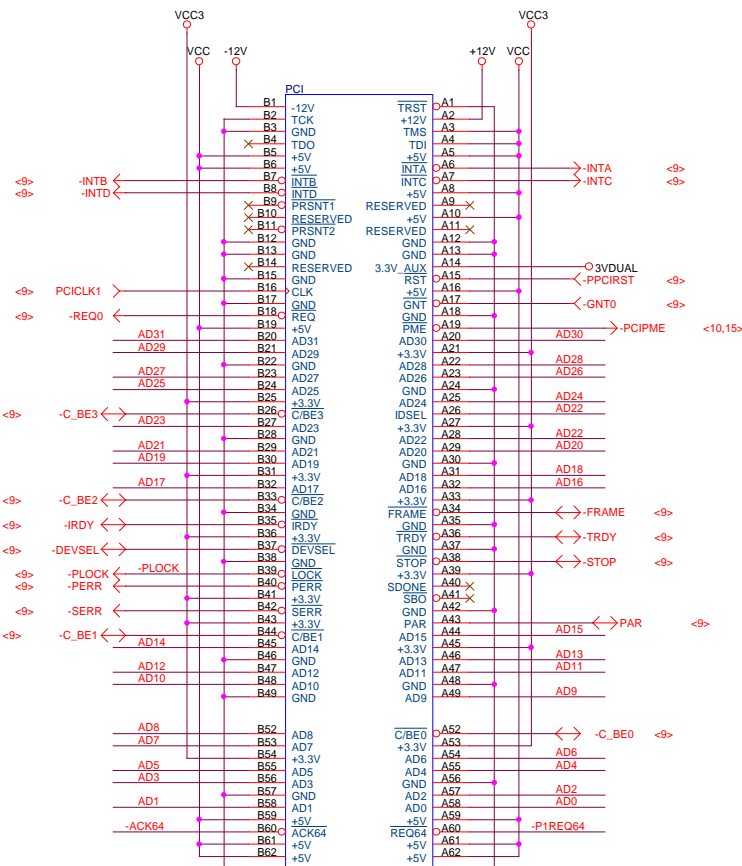
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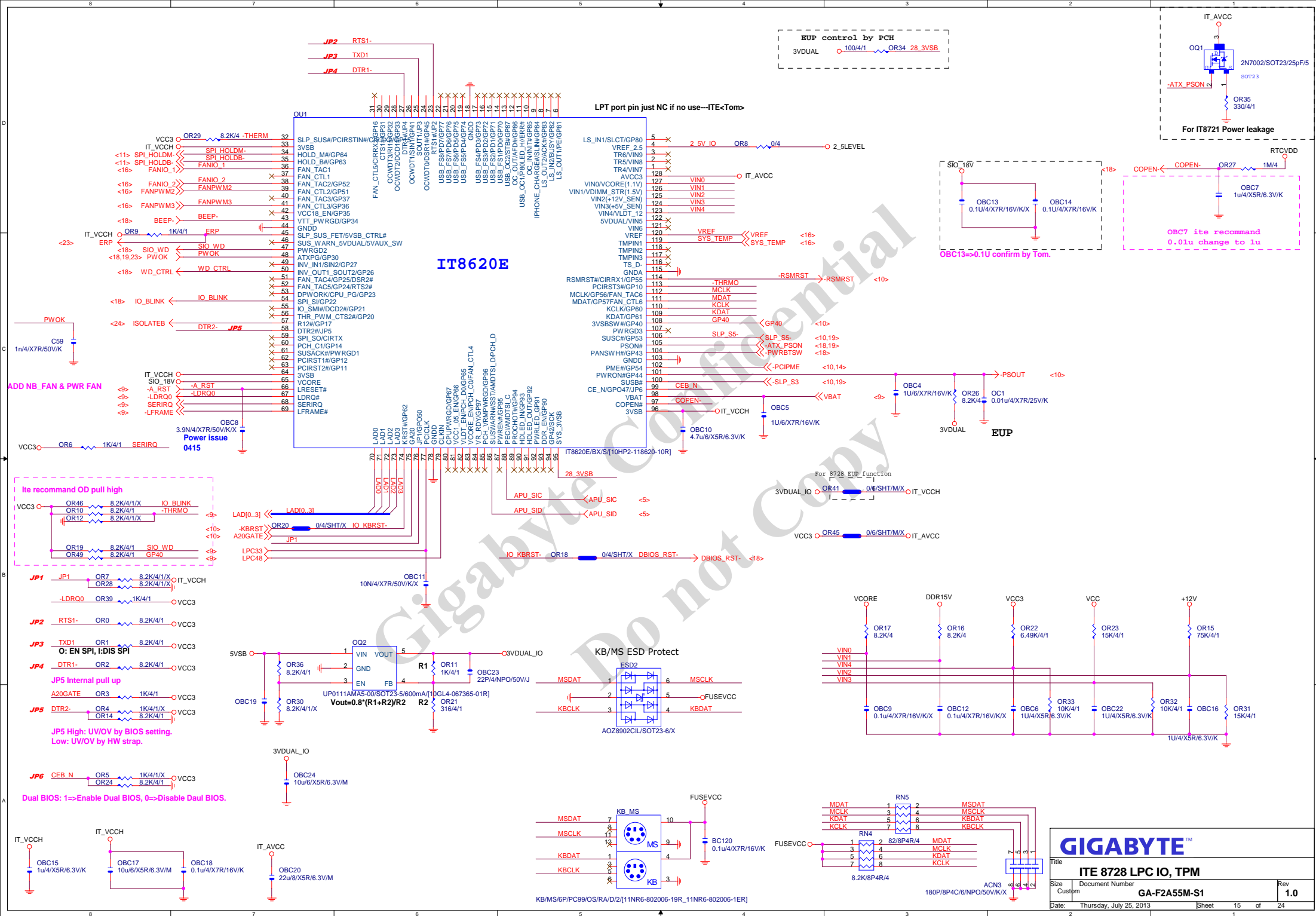
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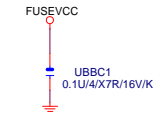
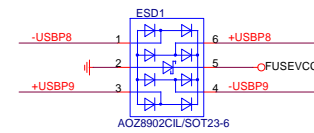
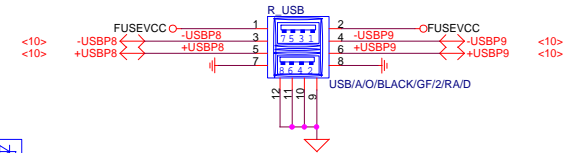
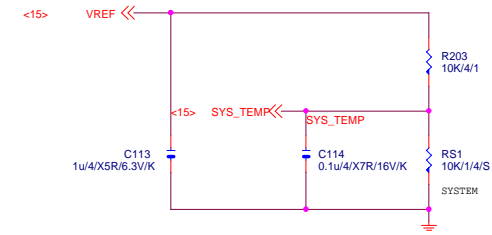
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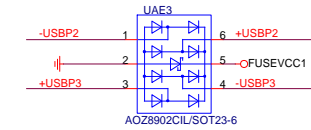
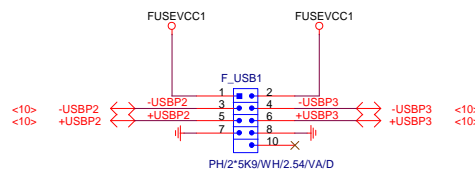
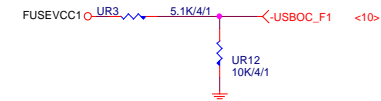
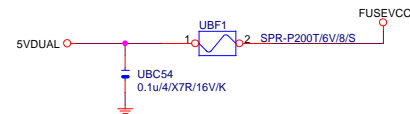




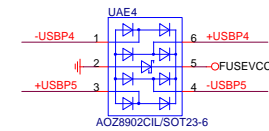
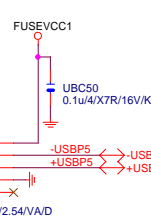
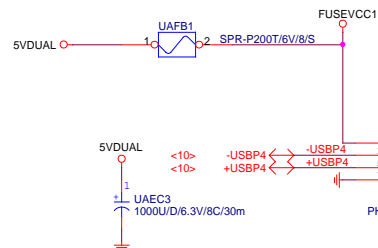
Hardware Monitor circuits



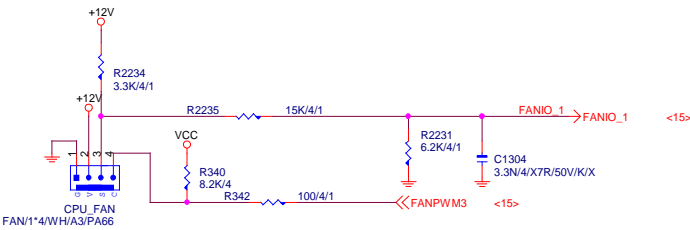
FRONT SIDE USB1



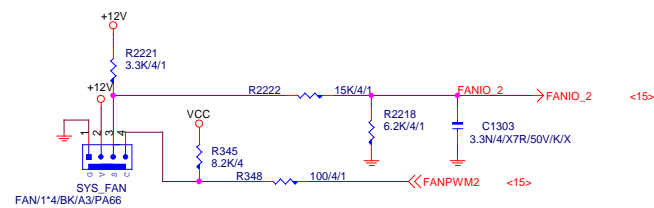
FRONT SIDE USB2



CPU FAN



SYSTEM FAN



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